CRYSTAL CLOCK OSCILLATORS



Differential Positive ECL (DPECL) SK-2910 Series

Rev. C

Description

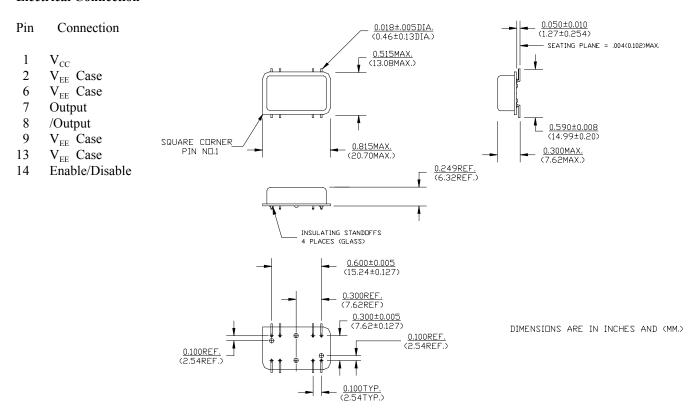
The **SK-2910 Series** of quartz crystal oscillators provide DPECL Fast Edge compatible signals. This device is to operate using positive voltage and uses multiple ground pins for improved signal integrity. This device is intended to operate on positive voltage for PECL applications.

Features

- Wide frequency range-122.0MHz to 300.0MHz
- · User specified tolerance available
- · Case at electrical ground
- Will withstand vapor phase temperatures of 253°C for 4 minutes maximum
- All metal, resistance weld, hermetically sealed package
- · High shock resistance, to 1500g

- Low Jitter
- Overtone technology
- · High Q Crystal actively tuned oscillator circuit
- Power supply decoupling internal
- · Dual ground plane for added stability
- No internal PLL avoids cascading PLL problems
- High frequencies due to proprietary design

Electrical Connection





357 Beloit Street, P.O. Box 457, Burlington, WI 53105-0457 U.S.A. Phone 262/763-3591 FAX 262/763-2881 Email: nelsales@nelfc.com www.nelfc.com

SK-2910 Series Continued Differential Positive ECL (DPECL) Rev. C

Operating Conditions and Output Characteristics

Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max
Frequency			122.0MHz		300.0MHz
Duty Cycle		@ 50% points	45/55%		55/45%
Logic 0 (2)	V_{OL}		V _{cc} -1.95V		V_{cc} -1.60V
Logic 1 ⁽²⁾	V _{OH}		V _{CC} -1.02V		V_{CC}^{00} -0.74V
Rise & Fall Time	tr,tf 20-80%V	$_{ m o}$ with 50 ohm load to ${ m V}_{ m cc}$ -2 ${ m V}$	/	350 psec	600 psec
Jitter, RMS ⁽³⁾					5 psec
Enable Voltage ⁽⁴⁾		with V _{FF} =0V	0V		1.0V
Disable Voltage		with V _{EE} =0V	3.0V		V_{cc}
Frequency Stability (1)	voltage	conditions including: e, calibration, temp., ging, shock, vibration	-100ppm		+100ppm

General Characteristics

Parameter Supply Voltage Supply Current	$\begin{array}{c} \text{Symbol} \\ V_{\text{cc}} \\ I_{\text{cc}} \end{array}$	Conditions 50 ohm termination To 2.00V below V _{CC}	Min 4.75V 0.0 mA	Typical 5.0V 	Max 5.25V 120 mA
Output current Operating temperature Storage temperature Input: Logic High (ECL) - V _{EE} or Open - Enab			0.0 mA 0°C -55°C		±50.0 mA 70°C 125°C
Lead temperature Load Start-up time	T _I	Soldering, 10 sec. V or Thevenin Equivalent, E	 Bias Required 	2 ms	300°C 10 ms

Environmental and Mechanical Characteristics

Mechanical Shock Per MIL-STD-202, Method 213, Condition E Thermal Shock Per MIL-STD-833, Method 1011, Condition A

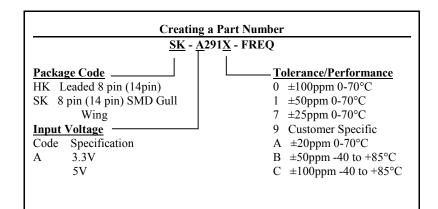
0.060" double amplitude 10 Hz to 55 Hz, 35g's 55Hz to 2000 Hz Vibration

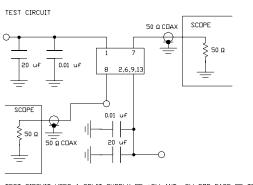
Soldering Condition 300°C for 10 seconds

Leak rate less than 1 x 10 -8 atm.cc/sec of helium Hermetic Seal

Footnotes:

- 1) Standard frequency stability (±20,±25,±50ppm & others available)
- 2) V_{OL}, V_{OH}, referenced to ground
- 3) Jitter performance is frequency dependent. Please contact factory for full characterization.
- 4) Open to Enable pin also enables to output.





TEST CIRCUIT USES A SPLIT SUPPLY OF +2V AND -3V FOR EASE OF TESTING.

